



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/755,673
Confirmation No. None
Filing Date January 5, 2001
Inventor Leonard Forbes, et al.
Assignee Micron Technology, Inc.
Group Art Unit 2823
Examiner Nguyen, K.D.
Attorney's Docket No. MI22-1531
Customer No. 021567
Title: Methods of Forming Capacitor Structures, and Capacitor Structures

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -See Attached Form PTO-1449

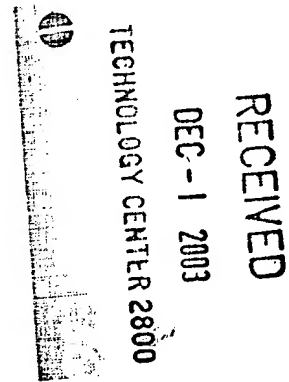
The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

Respectfully submitted,

Dated: 11/16/03

By: [Signature]
Robert C. Hyta
Reg. No. 46,791

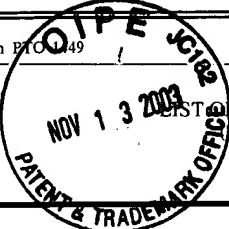


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Form PTO 49		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-1531	SERIAL NO. 09/755,673		
				APPLICANT Leonard Forbes et al.			
				FILING DATE January 5, 2001	GROUP 2823		
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AM	JP200058777A	02/00	Japan - electronic translation and abstract			X
	AN						
	AO						
	AP						
	AQ						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
	AR						
	AS						
EXAMINER				DATE CONSIDERED			
<p><small>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small></p>							

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